

## 12-CHANNEL 1/2/4G FIBRE CHANNEL INTELLIGENT LOOP CIRCUIT WITH EYEOPENER®

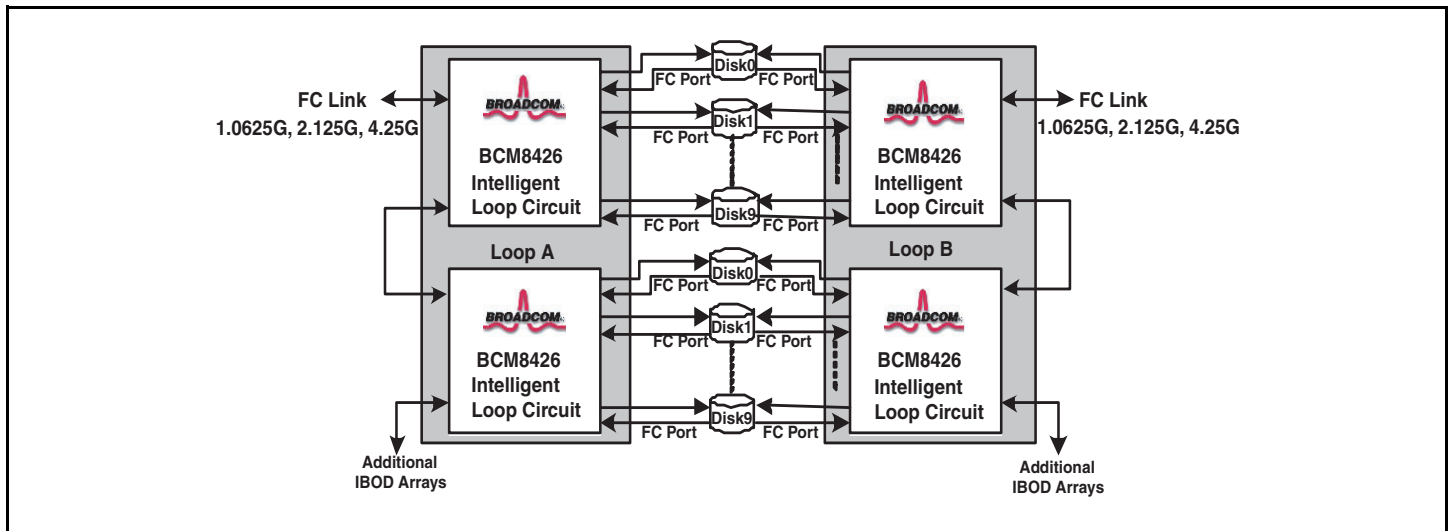
### FEATURES

- 12-channel intelligent loop circuit to configure fibre channel hard disk drives in FC-AL loops
- Integrated EyeOpener per channel exceeding fibre channel signalling specifications
- Multi-rate support for new and legacy systems
- Active-Signal Integrity (Active-SI) monitors physical layer and Active-Link Integrity (Active-LI) monitors link-layer performance
- Programmable transmit output levels and receive LOS detection
- High performance receive equalization and programmable transmit pre-emphasis
- Developed using advanced 0.13u CMOS process technology

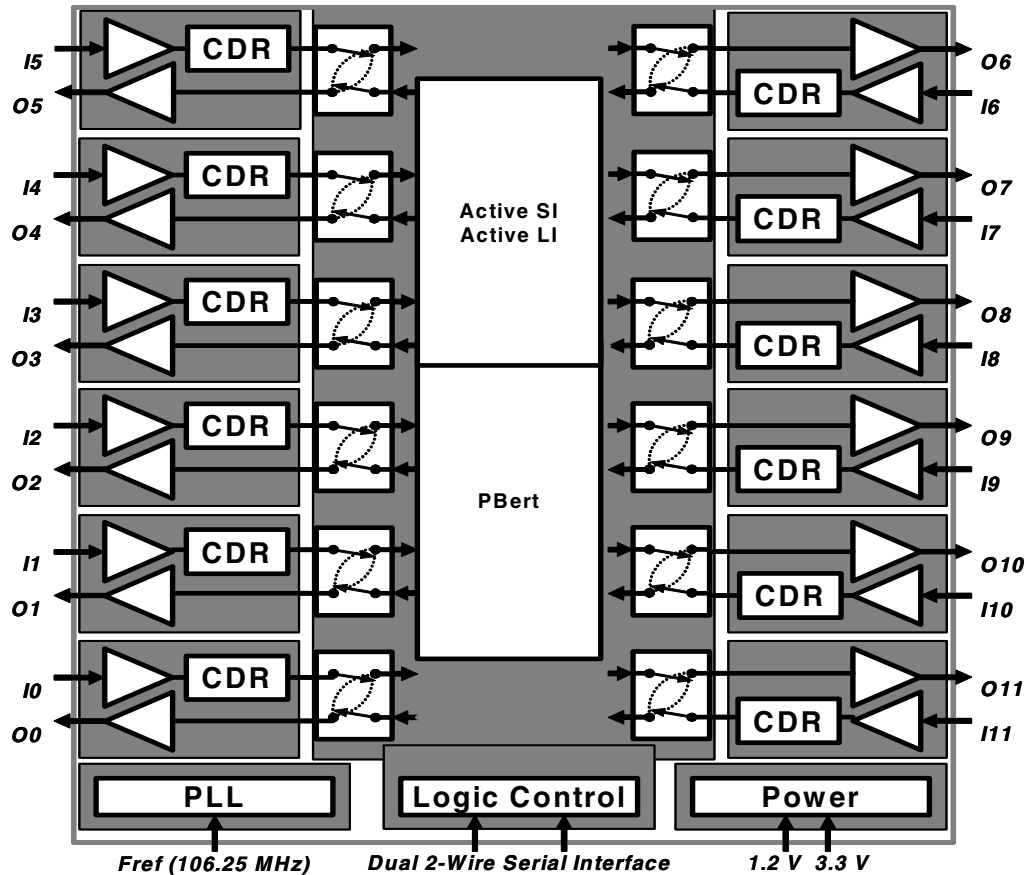
### SUMMARY OF BENEFITS

- Low-cost back end solution for fibre channel disk drive arrays
- Guarantees jitter and bit-error-rate performance on optical and fiber links
- Supports legacy 1.0625 Gb and 2.125 Gb applications, and new 4.25G data rates off a single reference clock frequency
- On-chip intelligent diagnostics provides insight to the signal quality with detection and recovery mechanisms on each high-speed link
- Optimizes channel performance for lower power and signal detection through on-chip 2-wire control interface
- Transmits and receives over long distances of copper cables or backplanes with 16 levels of programmable pre-emphasis
- Low power design of ~4W with small footprint package in a 21 mm x 21 mm BGA, with 1 mm ball pitch

**BCM8426 Functional Block Diagram**



## OVERVIEW



The **BCM8426** is an intelligent back end loop circuit that facilitates the implementation of an IBOD (Intelligent Bunch of Disks) on Fibre Channel Storage Array Systems. The **BCM8426** has an integrated EyeOpener<sup>®</sup> designed on-chip to drive optical and copper ports in fibre channel applications.

Each port of the **BCM8426** can be independently configured as a retimer or repeater operating at the fibre channel data rates of 1.0625 Gbps, 2.125 Gbps, and 4.25 Gbps. With an on-chip low-jitter phase-locked loop (PLL), the **BCM8426** exceeds fibre channel signal quality specifications.

The **BCM8426** features Active Signal Integrity (Active-SI), which allows monitoring of the received signal at each high-speed port to determine signal eye quality and bit error rate (BER) for system diagnostics, fault isolation, optics aging, and

maintenance. An on-chip pattern generator/checker is provided with internal loopback capability on each I/O port.

The **BCM8426** also features Active Link Integrity (Active-LI), which is a set of advance link integrity functions performing data generation and checking on transmitted fibre channel data. The combination of Active-SI and Active-LI improves storage system's Reliability, Availability, and Serviceability (RAS) with the ability to monitor and diagnose system integrity.

Data from each port can be optionally retimed utilizing an elastic FIFO and local crystal reference clock eliminating jitter transfer. The **BCM8426** can also be configured to operate in repeater mode for low latency.

The **BCM8426** is available in a 21 mm x 21 mm BGA package.

**Broadcom<sup>®</sup>**, the pulse logo, and **Connecting everything<sup>®</sup>** are trademarks of Broadcom Corporation and/or its subsidiaries in the United States and certain other countries. All other trademarks mentioned are the property of their respective owners.

Connecting  
**everything<sup>®</sup>**



**BROADCOM CORPORATION**  
16215 Alton Parkway, P.O. Box 57013  
Irvine, California 92619-7013

© 2004 by BROADCOM CORPORATION. All rights reserved.

8426-PB00-R 02/20/04

Phone: 949-450-8700  
Fax: 949-450-8710  
E-mail: [info@broadcom.com](mailto:info@broadcom.com)  
Web: [www.broadcom.com](http://www.broadcom.com)

This datasheet has been download from:

[www.datasheetcatalog.com](http://www.datasheetcatalog.com)

Datasheets for electronics components.